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TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		U.S. APPLICATION NO. (if known) see 37 C.F.R. 1.51 <b>10/030342</b>	
INTERNATIONAL APPLICATION NO. PCT/AU00/00796	INTERNATIONAL FILING DATE 30 June 2000	PRIORITY DATE CLAIMED 30 June 1999	
TITLE OF INVENTION A SCALABLE COMPUTER SYSTEM			
APPLICANT(S) FOR DO/EO/US Fergus O'BRIEN; Matthew ROUGHAN			
Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:			
1. <input checked="" type="checkbox"/> This is a <b>FIRST</b> submission of items concerning a filing under 35 U.S.C. 371.			
2. <input type="checkbox"/> This is a <b>SECOND</b> or <b>SUBSEQUENT</b> submission of items concerning a filing under 35 U.S.C. 371.			
3. <input type="checkbox"/> This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).			
4. <input checked="" type="checkbox"/> A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.			
5. <input checked="" type="checkbox"/> A copy of the International Application as filed (35 U.S.C. 371(c)(2))			
a. <input checked="" type="checkbox"/> is transmitted herewith (required only if not transmitted by the International Bureau).			
b. <input type="checkbox"/> has been transmitted by the International Bureau.			
c. <input type="checkbox"/> is not required, as the application was filed in the United States Receiving Office (RO/US)			
6. <input type="checkbox"/> A translation of the International Application into English (35 U.S.C. 371(c)(2)).			
7. <input checked="" type="checkbox"/> Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371(c)(3))			
a. <input checked="" type="checkbox"/> are transmitted herewith (required only if not transmitted by the International Bureau).			
b. <input type="checkbox"/> have been transmitted by the International Bureau.			
c. <input type="checkbox"/> have not been made; however, the time limit for making such amendments has NOT expired.			
d. <input type="checkbox"/> have not been made and will not be made.			
8. <input type="checkbox"/> A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).			
9. <input checked="" type="checkbox"/> An oath or declaration of the inventor(s) (35 U.S.C. 371(c)(4)) (signed)			
10. <input type="checkbox"/> An English language translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371(c)(5)).			
Items 11. to 16. below concern other document(s) or information included:			
11. <input checked="" type="checkbox"/> An Information Disclosure Statement under 37 CFR 1.97 and 1.98.			
12. <input type="checkbox"/> An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.			
13. <input type="checkbox"/> A FIRST preliminary amendment.			
14. <input type="checkbox"/> A SECOND or SUBSEQUENT preliminary amendment.			
15. <input type="checkbox"/> A substitute specification.			
16. <input type="checkbox"/> A change of power of attorney and/or address letter.			
17. <input type="checkbox"/> A computer-readable form of the sequence listing in accordance with PCT Rule 13.2 and 35 U.S.C. 1.821 - 1.825.			
18. <input type="checkbox"/> A second copy of the published international application under 35 U.S.C. 154(d)(4).			
19. <input type="checkbox"/> A second copy of the English language translation of the international application under 35 U.S.C. 154(d)(4).			
20. <input checked="" type="checkbox"/> Other items or information:			
COPY OF THE INTERNATIONAL SEARCH REPORT PREPARED BY THE AUSTRALIAN PATENT OFFICE; COPY OF THE INTERNATIONAL PRELIMINARY EXAMINATION REPORT; AND CONFIRMATION POSTCARD.			

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## A SCALABLE COMPUTER SYSTEM

### FIELD OF THE INVENTION

This invention relates to computer systems and more particularly, but not exclusively, to a computer system which is required to have a large number of computer processors for use in large scale system applications. The present invention has application to scalable telecommunications and intelligent networks.

### BACKGROUND ART

Recent developments in telecommunications and intelligent networks, primarily involving the field of photonics, are resulting in a rapid expansion of bandwidths available for communication. The available bandwidth is currently growing at a rate involving, roughly, a factor of two every two years, and it is anticipated that communication bandwidths may increase by at least three orders of magnitude over the next ten years.

In order to match this rapid bandwidth growth in the level of telecommunications, equivalent computing power is required. It is therefore desirable to design a computer based system architecture that is massively scalable. The scalability is essentially driven by the number of independent users, as in mobile phone devices, rather than the complexity or size of an individual application.

Hitherto, there has been no known solution to the problem of designing a massively scalable architecture for the intelligent network (IN) domain.

The Internet is a large computer system, but it is a hierarchical system and hence does not address the issue of massive scalability.

Other computer system architectures that are known are only scalable in many orders of magnitude less than what will be required in the near future in telecommunications and intelligent networks.

For example, the computer system of Figure 1 is based on an Erlang/Open Telecom Platform (OTP) running on a single node. The computer system includes hardware 12, an operating system 14, a display 16 and a keyboard 18 and a suite of programs 20 which include application programs 22 (eg. in programming languages, Erlang and C), sourced programs 24, run-time programs

26, a library 28, and a database 30. The system may be linked to an external database 32 if required.

The single node provides reasonable system development facilities, including an Erlang real time environment, or interpretive environment. However, this is achieved at the expense of potential performance owing to the interpreter/operating system layers.

The single node computer system of Figure 1 may be linked to other similar nodes by an asynchronous transfer node (ATM) switch, such as the AXD-301 switch with satisfactory performance. However, this switch has a scalability of 1:30, which is orders of magnitude less than that which is required for ultra high communication bandwidths.

Referring to Figure 2, there is shown a split node computer system in which an OTP node 34 is split into two closely couple nodes: a COTS (commodity of the shelf) system 40 and a multi-processor (MP) Erlang Engine 50. The COTS system is essentially the base system and may comprise of a UNIX operating platform 41, application programs (eg. in C, C++, Java and Erlang) 42, a disc drive 43, graphics 44, an Internet modem 45, an Internet interface (TCP/IP) 46 and an input/output interface (I/O) 47 for communicating with the Erlang Engine 50.

The Erlang Engine 50 is a shared memory MP system running software 52 and 54 in Erlang on top of an optimized message passing kernel (56) such as QNX. The Erlang Engine 50 also has an I/O interface 58 for communicating with the COTS system 40. One processor of the MP set can be devoted to monitoring software, the remainder to functional processing.

The split node OTP system 34 of Figure 2 may form part of a network that includes a plurality of regional processors (RP) 61 and support processors (SP) 62 for operators. The regional and support processors 61, 62 are connected to central processors CP A 63 and CP B 64 and to each other by a high speed RP bus 65. The MP Erlang Engine 50 includes a high speed interface 59 for communicating with the central processors CP A 63 and CP B 64.

The split node OTP system 34 can be linked to other computer systems by a switch 70 such as an AXE-10. For this purpose an AXE programming system (APS) 72 may be provided. In a telecommunications application, the interface

shown to the AXE-10 may be implemented as a high speed Ethernet, primarily due to the availability of the Ethernet PLEX (Programming Language for Exchanges) blocks existing on AXE-10. The Erlang to PLEX interface has been demonstrated in two modes, firstly with the AXE-10 controlling the links, as in a call forwarding application. The second mode is with OTP in control, with an application such as remote changes to AXE-10 tariff tables.

The scalability limit of the Erlang Engine is a maximum of eight processors, given that it is a shared memory environment. The eight processors, together with a demonstrated 5 x speed up from the move to compiled code, plus 2 x moving from Unix to QNX gives a scale-up of 80 from the base system.

Therefore it is apparent that both of these known systems have limitations in terms of their scalability and could not be considered massively scalable.

It is therefore desirable to provide a massively scalable computer system including a large number of processors in which each processor can communicate effectively with other processors without regard to their locations.

#### SUMMARY OF THE INVENTION

According to one aspect the present invention provides a telecommunications system having a plurality of nodes, such that the plurality of nodes are clustered in a plurality of interconnected neighborhoods, the system characterised in that a relatively small number of cross-links are provided between nodes of different neighborhoods.

According to another aspect, the present invention provides a large scale computer system including a multiplicity of nodes, each node having a plurality of interconnected processors, said nodes being arranged in a network with neighbouring sets of nodes of the network forming neighbourhoods of fully interconnected nodes, wherein random links are provided between nodes of different neighbourhoods in the network whereby each processor of the system can communicate effectively with other processors regardless of their location in the network without full connectivity in the network.

In essence, the present invention has come about through the realisation that problems associated with telecommunication and intelligent network

scalability and/or computer architecture design, can be addressed by applying the "small world" principle.

The "small world" concept was derived from the "six degrees of separation" principle. Six degrees of separation is a theory that a person may reach anyone in the world through at the most six personal contacts. This theory was explored through an Internet site based upon the actor Kevin Bacon. This site was created out of curiosity, but was a very useful sociological experiment for illustrating the six degrees of separation principle. The site however, gave no hint as to any underlying structure of the people involved in the interconnections.

The key to the underlying structure may be derived from studies into epidemics. For example diseases, such as the Black Death spread with great rapidity from one part of the world to another: from the Middle East to Europe. In that situation, the epidemic mechanism was that of an area that already had the disease within a defined group, and a single vector, such as ship-borne rats travelling to another defined group.

By understanding the relationship between epidemics and the six degrees of separation principle, it is apparent that the underlying structure of the six degrees of separation principle was a set of richly connected groups with a small number of inter-group links. It is this combination of richly connected neighborhoods, with a small number of links between them that is termed a "small world" network.

In other words, the present invention utilizes a "small-world" network architecture, in which a relatively small number of random cross-links of nodes or vertices in a network can result in small characteristic path lengths, for the transfer of messages between nodes or vertices in a telecommunications/computer network regardless of their location.

#### BRIEF DESCRIPTION OF THE DRAWINGS

A preferred embodiment of the present invention will now be described, by way of example only, with reference to the accompanying drawings in which: -

Figure 1 is a diagram of a prior art single node computer system, which has limited scalability;

Figure 2 is a diagram of a prior art split node computer system;

Figure 3 is a diagram showing the difference between a "small world" network and regular ring and random networks;

Figure 4 is a graph showing variations in clustering and average path length with increasing randomness in a ring network.

Figure 5 is a diagram of a four-node computer system having four processors per node.

Figure 6 is a diagram of a small-world architecture for a computer system in accordance with the invention.

#### DETAILED DESCRIPTION

The "small world" principle is usually considered to apply to many biological and social networks, as these systems generally exhibit properties that are not completely regular or completely random but somewhere in between. For example, infectious diseases exhibit "small world" properties. In this regard, it has been shown that infectious diseases spread more easily in small world networks than regular lattices.

The present invention applies this small world principle to telecommunications/computer networks. A comparison of the performance of a small world telecommunications network, to that of regular and random telecommunications networks will now be provided. Examples of regular, small-world and random networks are shown in Figure 3.

In very large telecommunications networks with only local connections most vertices or nodes in the network are separated by many links. In regular lattice or ring structures with local connections between vertices or nodes, the characteristic or mean distance of the path length ( $L$ ) between two vertices or nodes grows approximately linearly with the size of the network. On the other hand, in a network with only random connections between vertices or nodes, the characteristic path length ( $L$ ) grows logarithmically with the number of vertices or nodes. A network with only random connections, however, is a poorly clustered environment, as a sparse number of random connections can result in some vertices or nodes not being inter-connected to other nodes at all.

A small world telecommunications network in accordance with the present invention is one in which vertices or nodes in local "neighborhoods" or "clusters"

are interconnected to each other, and a relatively small number of random links or connections are provided between nodes of different neighborhoods of the network.

In "Collective dynamics of "small-world networks" by Duncan J. Watts & Steven H. Strogatz, Nature, Vol. 393, pp. 440-442, a mathematical approach to defining structural properties of small-world networks was provided in terms of characteristic path length  $L(p)$  and clustering coefficient  $C(p)$ .  $L(p)$  measures the typical separation between two vertices in a network (a global property) and  $C(p)$  measures the clustering or connections in a typical neighborhood (a local property), where  $p$  is a probability factor.

In a regular network having  $n$  nodes and  $k$  edges or links per nodes,  $p = 0$ ,  $L(0)$  grows linearly with  $n$ , the number of nodes in the network, and  $C(0)$  depends on the specific geometry or 'wiring' of the network.

In a completely random network with only random connections,  $p = 1$  and  $L$ -random grows logarithmically with  $n$ , whereas  $C$ -random  $\approx k/n \ll 1$ .

"Small-world" networks, on the other hand, have a broad interval of  $p$  over which  $L(p)$  is almost small as  $L$  random, yet  $C(p)$  is much greater than  $C$ -random. This is shown in the graph of Figure 4.

Watts and Strogatz thus demonstrated numerically that a few random global connections are sufficient to turn a regular network into a small-world network to reduce the path length of the number of links for effective communication between nodes or vertices drastically.

Hanspeter Herzel in his article entitled "How to Quantify Small-World Networks" (Fractals, Vol. 6, No. 4 (1998) 301-303) derived a formula for the mean connectivity  $k^{\text{eff}}$  of nodes in a small-world ring network. For a ring network with  $k$  edges of links per node, in which a fraction of  $p$  connections are re-wired randomly the formula was:

$$k^{\text{eff}} = k \cdot p$$

When  $k^{\text{eff}} > 1$ , the characteristic path length  $L(p)$  may be expressed as follows:



$$L(p) = \frac{\ln(n)}{\ln(k^{\text{eff}})} = \frac{\ln(n)}{\ln(k) + \ln(p)}$$

Such a logarithmic curve approximates the curve  $L(p)/L(0)$  in Figure 4.

In the present invention, the number of nodes in the network, the number of nodes in the neighborhood, the number of nodes per neighborhood and the number of cross-links in the network may each be varied for different applications, provided that the network functions as a small-world network with large clustering  $C(p)$  and small average path lengths  $L(p)$ .

The cross-links in the small-world network of the present invention may be chosen completely at random. Alternatively, a pseudo-random selection process may be used to select the cross-links between neighborhoods to convert a regular network into a highly clustered small-world network with a relatively small average path length. In this regard, an acceptable result in terms of the number of hops per inter-process message, from a system process point-of-view, is under two.

In one preferred embodiment of the invention a mean connectivity falling substantially in the range from about 1.5 to about 2.0, and preferably about 1.6, may be achieved by appropriate choice of the number of nodes per neighborhood, the connectivity of each neighborhood and/or the number of cross-links relative to the total number of nodes in the network. By way of example, in a computer system having 50 neighborhoods of 10 nodes arranged in a ring network with about 50 cross-links between neighborhoods, each node is connected to 9 other nodes in its neighborhood ( $k = 9$ ) and the probability factor  $p = 50 \times 2/500$ . Thus

$$k^{\text{eff}} = 9 \times \frac{100}{500} = 1.8$$

The number of interconnected processors in each node of the computer system may also vary for different applications. With the development of photonics, it is envisaged that up to about 256 nodes, each containing up to eight processors per node may be fully connected together by optical fibres and a high speed switch to form a single neighborhood in a small-world network. With a large number of such neighborhoods in the small-world network connected by a relatively small number of cross-links, it is possible to achieve a massively

scalable computer system with a very large number of total processors in the system which are able to communicate with each other in an effective manner.

There will now be explained with reference to Figure 6, an example of how a "small-world" network architecture can be used to achieve a large scale computer system of about one million processors in which each processor of the system is able to communicate effectively with other processors in the system without requiring full connectivity.

Figure 6 shows a small-world telecommunications network in which there are 20 nodes arranged in a ring layout. The 20 nodes of the ring network can be considered to form five neighborhoods of nodes A, B, C, D and E in which there are four nodes (A<sub>1</sub> to A<sub>4</sub>; B<sub>1</sub> to B<sub>4</sub>; C<sub>1</sub> to C<sub>4</sub>; D<sub>1</sub> to D<sub>4</sub>; and E<sub>1</sub> to E<sub>4</sub>) per neighborhood.

Each node eg A<sub>2</sub> is connected to adjacent nodes A<sub>1</sub>, A<sub>3</sub> by an edge link EL and alternate nodes eg A<sub>1</sub>, A<sub>3</sub>; A<sub>2</sub>, A<sub>4</sub>; A<sub>3</sub>, B<sub>1</sub> are connected to each other by a small loop link LL.

A network connected only in this manner would normally be termed a regular network in which messages from one node, eg A<sub>1</sub>, must pass through a relatively large number of links to reach a node at the opposite side of the network, eg C<sub>4</sub>.

A small-world network such as shown in Figure 6, differs from a regular network in that a relatively small number of random cross-links are provided between neighborhoods of nodes. In the example of Figure 6, there are three cross-links CL<sub>1</sub>, CL<sub>2</sub> and CL<sub>3</sub>. Cross-link CL<sub>1</sub> directly connects nodes A<sub>2</sub> and C<sub>1</sub> with each other; cross-link CL<sub>2</sub> directly connects nodes B<sub>2</sub> and E<sub>3</sub> with each other and cross-link CL<sub>3</sub> directly connects nodes D<sub>1</sub> and E<sub>4</sub> with each other. It will be apparent from Figure 6 that there is a marked increase in connectivity with only a relatively small number of cross-links between neighborhoods of nodes. For instance, a message to be sent from node A<sub>1</sub> to C<sub>2</sub> can pass along an edge link EL to node A<sub>2</sub>, along cross-link CL<sub>1</sub> to C<sub>1</sub> and then along an edge link to C<sub>2</sub>. This is a clear improvement over a regularly connected network, where the message would pass along four loop links to C<sub>1</sub> and an edge link EL to C<sub>2</sub>. Also, a message from B<sub>4</sub> to E<sub>2</sub> can pass along a loop link to B<sub>2</sub>, along cross-link CL<sub>2</sub> to E<sub>4</sub> and then along another loop link LL to E<sub>2</sub>, rather than along five loop links.

It will be seen from Figure 6 that a small-world network is an architecture that can be used to link together a relatively large number of computer processors while retaining effective connectivity between the processors.

In a preferred embodiment of the invention, each neighborhood of Figure 6 corresponds to the four-node/four-process per node computer system shown in Figure 5. Referring to Figure 5, there is shown a system that demonstrates it is possible to link up several MP Erlang Engines of the type shown in Figure 2 through a high-speed blocking switch. Each node of the computer system of Figure 5 is an Erlang Engine 150 with four processors 152 running on a QNX kernel 156. Each Erlang Engine 150 may include a link to an OTP system 134 in similar manner to split node system of Figure 2 and may also have a local Internet connection 145.

The Erlang Engines 150 are connected to each other by optical interfaces 160, optical fibres 165 and a common high speed non-blocking optical switch 170. Each optical interface 160 may include at least one optical receiver 161 and one or more optical transmitters 162.

Currently available interface blocks 160 can handle 32 separate wavelengths: this is predicted to rise to 256 with further developments in photonics. This will permit up to 256 nodes (Erlang Engines) in a computer system such as that of Figure 5, as each node can be essentially addressed by an individual wavelength.

A key attribute of the system of Figure 5 is that the interface blocks can transmit one or more wavelengths, up to 256, with the same data simultaneously as a multicast facility. However, each node only receives on one wavelength: its address.

Since the system gives almost linear scaling on separable problems, 256 nodes can give a speed-up of 20,000 over the basic system.

It will be noted that the computer system of Figure 5 is a fully connected system in the sense that each Erlang Engine 150 has its own dedicated optical fibre link 165 to the central switch 170.

Applying the system of Figure 5 to the small world network of Figure 6, it is apparent that even where each neighborhood is a four node/four processors per

node system, a total of eighty processors can be effectively linked together by the 20 node network of Figure 6 with only 3 cross-links.

It is however, to be appreciated that the small-world network of Figure 6 is, however, only a relatively small-scale example of such a network. Within the scope of the present invention it is contemplated that at least 500 neighborhoods of nodes each having up to 256 fully connected Erlang Engines per node with 8 processors per Erlang Engine could be linked together in a small-world network to achieve a total system of over one million processors, with the small-world network architecture providing effective connectivity between the nodes with only a relatively small number of cross-links, say 50, between the neighborhoods. For example, the total number of individual Erlang processors running on such a system will be of the order of 256,000,000 assuming 2000 active processors per Erlang Engine node. This gives around 25.6 million lines of code, which is of the order of magnitude already envisaged for large software systems project.

It is envisaged that a massively scalable computer system in accordance with the invention has widespread applications. In the telecommunications field, the scalability could cover applications such as mobile telephone services for stockbroking, betting and other services. The invention may also be applied to Internet proxy servers in order to accommodate an escalating number of users.

More particularly in relation to the mobile telecommunications field, the present invention may be used as a "personal information manager in the sky" whereby it coordinates communications with handheld communications devices, such as Personal Digital Assistants (PDAs).

A further application is microbank applications, whereby the present invention may be used by financial institutions in order to allow them to economically handle extremely large numbers of transactions, particularly relating to very small amounts.

It will be appreciated that various modifications and alterations may be made to the present invention as described above without departing from the scope and spirit of the present invention. For instance, as mentioned above, the size of the network, the neighborhoods in the network, the number of nodes per

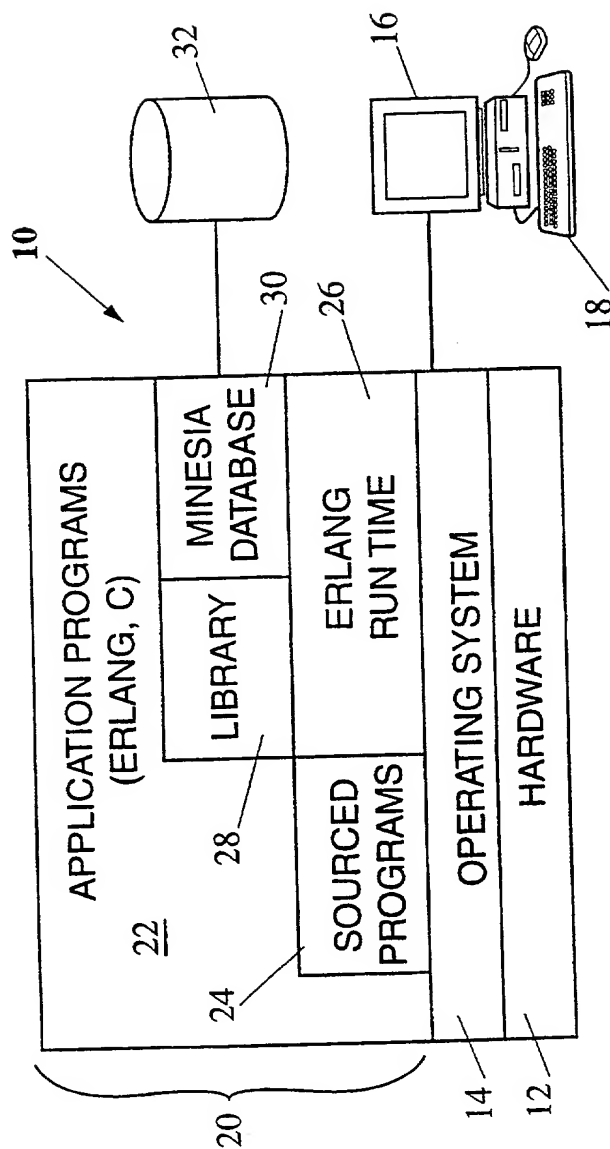
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THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOWS:

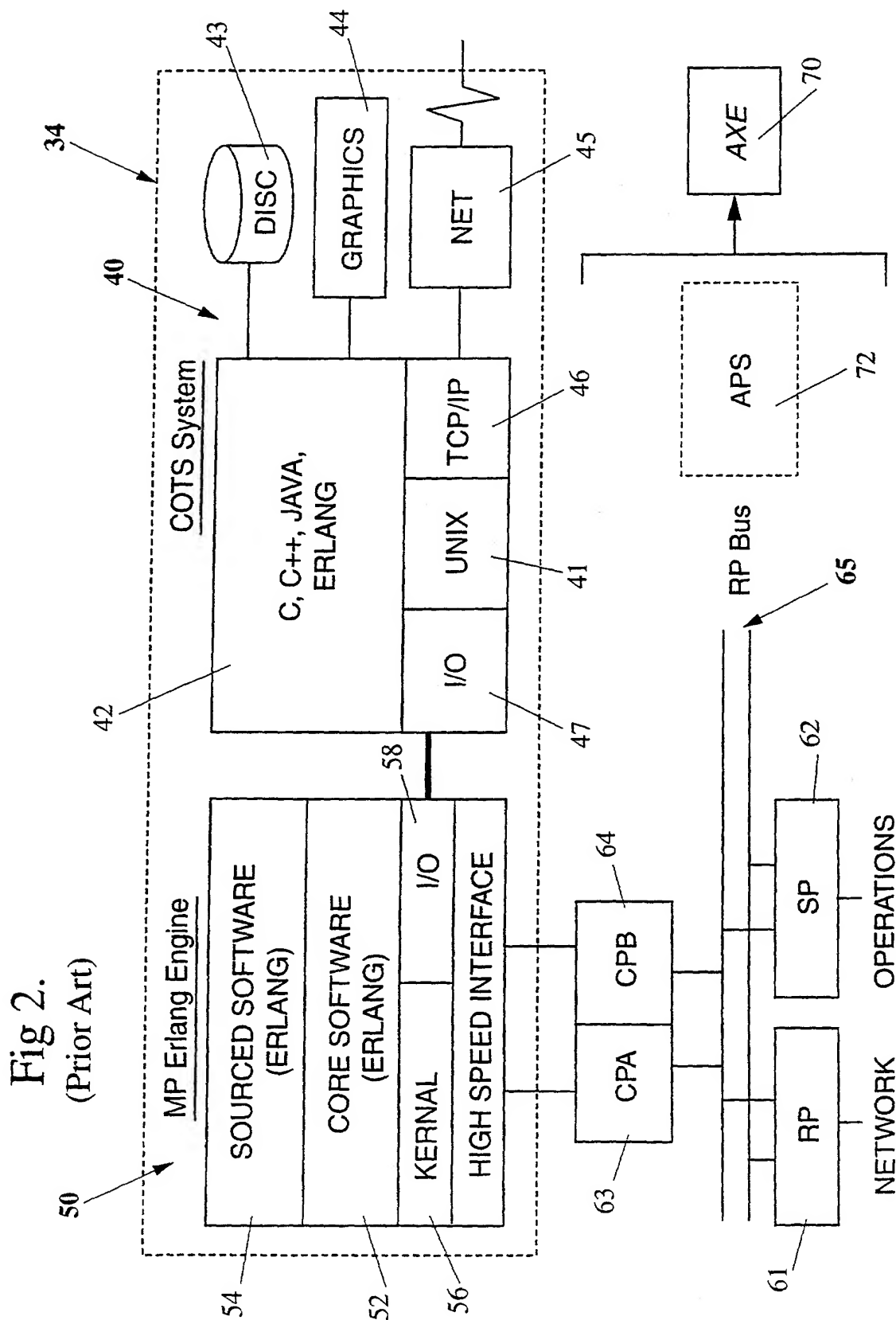
1. A telecommunications/computer system having a plurality of regularly connected nodes, such that the plurality of nodes are clustered in a plurality of interconnected neighborhoods, the system characterised in that a relatively small number of cross-links are provided between nodes of different neighborhoods.
2. The system of claim 1 wherein the relatively small number of connections are random links.
3. The system of claim 1 or 2 wherein the neighborhoods are fully interconnected.
4. The system of claim 1 wherein the mean connectivity between nodes of different neighborhoods is in the range of about 1.5 to 2.0.
5. The system of claim 1 wherein the mean connectivity between nodes of different neighborhoods is about 1.6.
6. A large scale computer system including a multiplicity of nodes, each node having a plurality of interconnected processors, said nodes being arranged in a network with neighboring sets of nodes of the network forming neighborhoods of fully interconnected nodes, wherein random links are provided between nodes of different neighborhoods in the network whereby each processor of the system can communicate effectively with other processors regardless of their location in the network without full connectivity in the network.
7. A scalable computer system formed using a small world principle.

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Fig 1.  
(Prior Art)



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Fig 3.

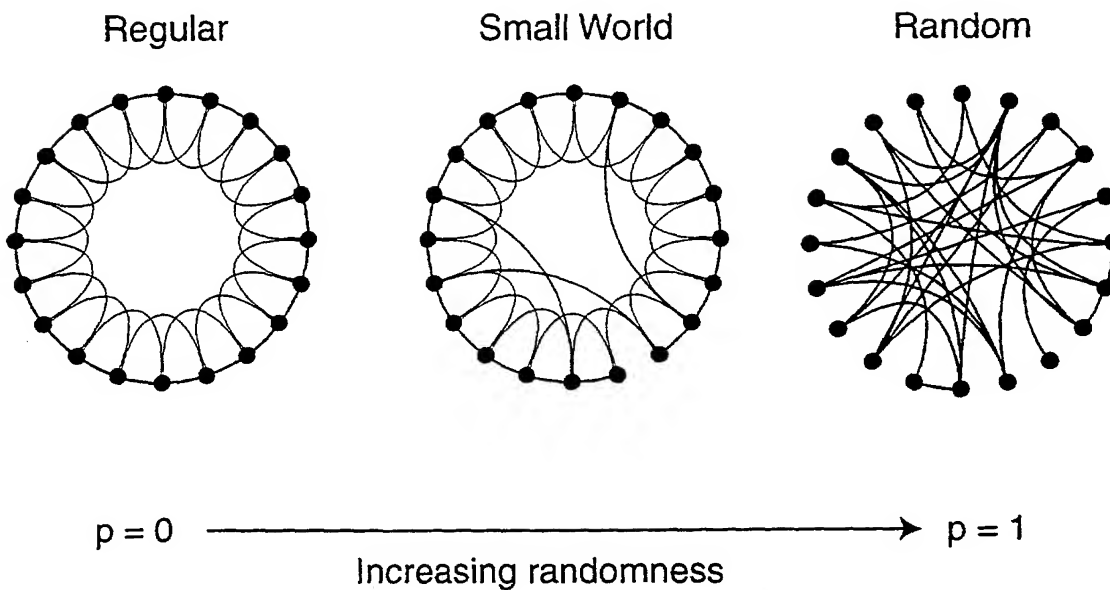
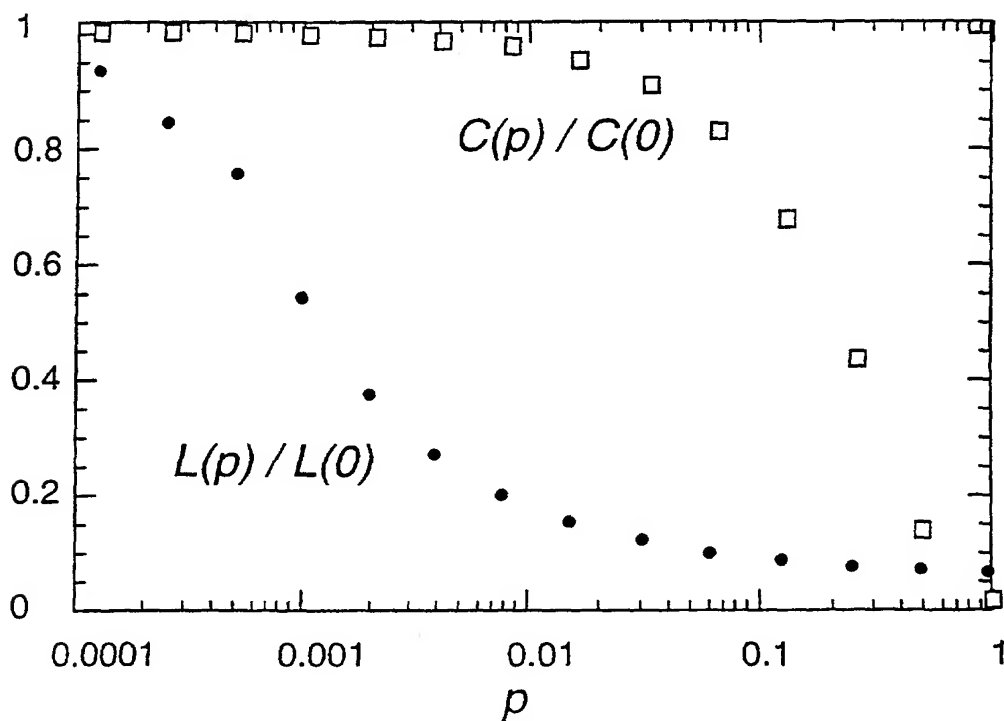
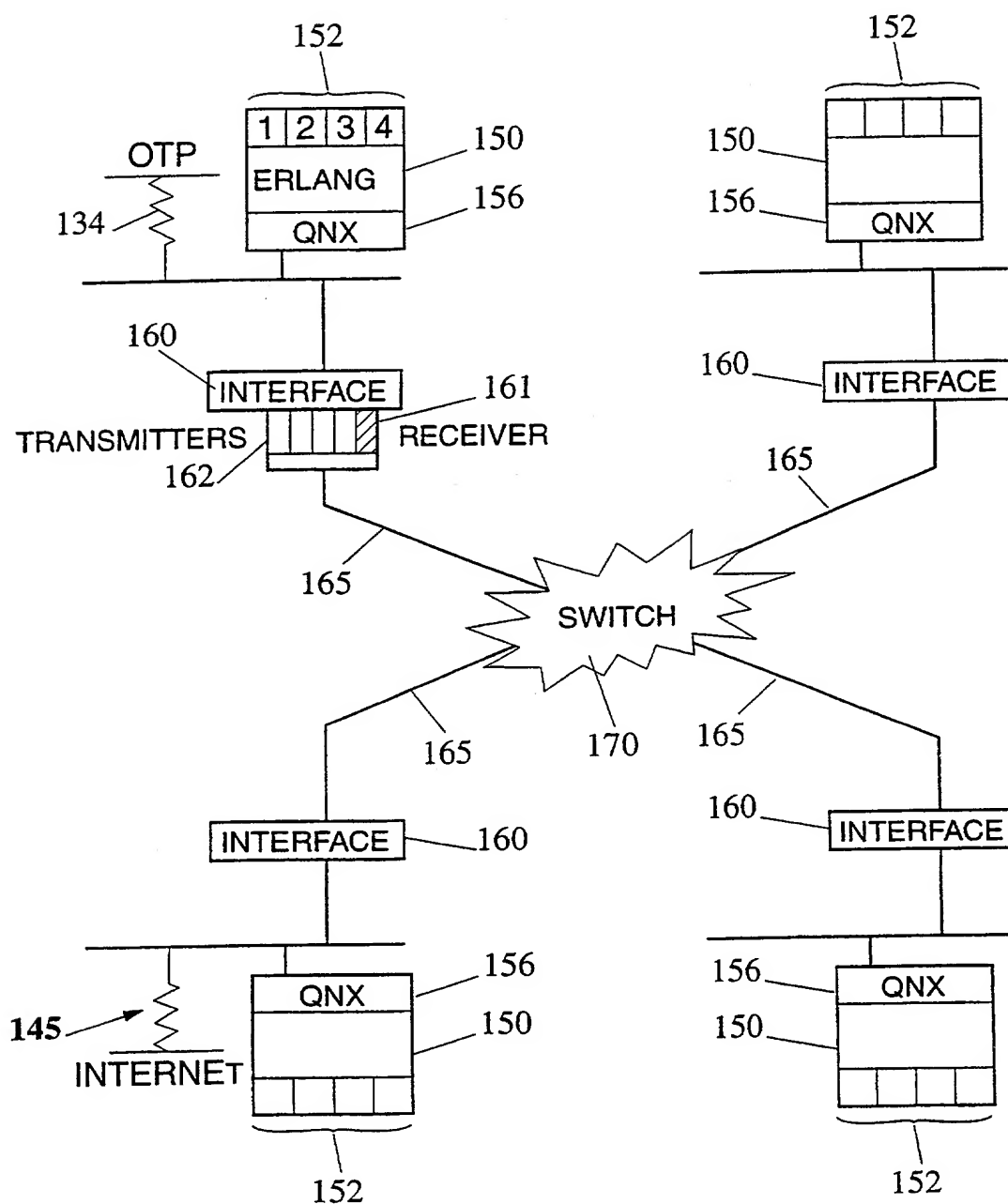


Fig 4.



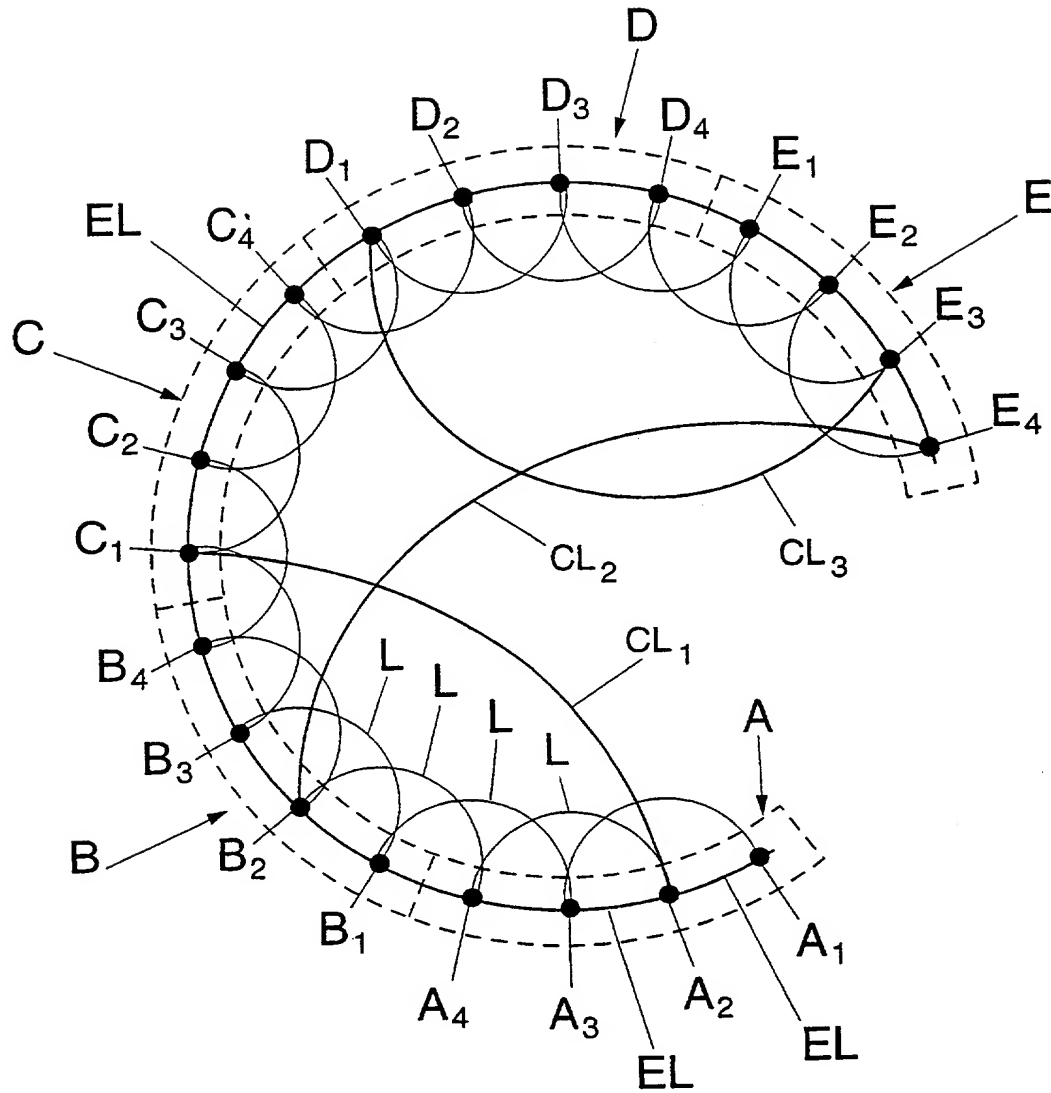
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Fig 5.



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Fig 6.



## COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; and

I verily believe that I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: \_\_\_\_\_

A Scalable Computer System

the specification of which:

\_\_\_\_\_ is attached hereto.

x was filed on 30 June 2000 / International PCT/AU00/00796  
as Application Serial No. / \_\_\_\_\_ and was amended  
on \_\_\_\_\_ (if applicable) (Attorney Docket No. \_\_\_\_\_.)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the Office all information known to me to be material to the patentability of this application as defined in 37 CFR § 1.56.

I hereby claim foreign priority benefits under 35 U.S.C. § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of any application on which priority is claimed:

Country	Number	Date Filed	Priority Claimed
<u>Australia</u>	<u>PQ 1286</u>	<u>June 30, 1999</u>	<u>x yes</u>
_____	_____	_____	_____

I hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 U.S.C.

1-00

2-00

10030342 122001

1	<u>Fergus O'Brien</u>	<i>Fo'Brien</i>	7-6-00 10.12.2001
	<b>Full Name</b>	<b>Inventor's Signature</b>	<b>Date</b>
	Melbourne. <u>Victoria</u> Australia. <i>AUX</i>		
2	<u>Matthew Roughan,</u>	<i>M Roughan</i>	<del>2008-02-01</del> 11.12.2001
	<b>Full Name</b>	<b>Inventor's Signature</b>	<b>Date</b>
	North Carlton. <u>Victoria</u> Australia. <i>AUX</i>		
3			
	<b>Full Name</b>	<b>Inventor's Signature</b>	<b>Date</b>
4			
	<b>Full Name</b>	<b>Inventor's Signature</b>	<b>Date</b>
5			
	<b>Full Name</b>	<b>Inventor's Signature</b>	<b>Date</b>

§ 112, I acknowledge the duty to disclose to the Office all information known to me to be material to patentability of the application as defined in 37 CFR § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Application Serial No.

Filing Date

Status (patented, pending)

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